		EAST Scare		- 15	Plurals	Time Stamp
·	Uita VSoari	ch Query	DBs	Default Operator	Piulais	
ef,			US-PGPUB;	OR	ON	2007/05/17 17:35
11	ccls.	water alignment) negre	USPAT US-PGPUB;	OR	ON	2007/05/17 17:48
12	mar nch	k same (semiconductor vision board circuit)	US-PGPUB;	OR	ON	2007/05/17 17:49
_13	ma (se	ference wafer alignment) near2 rk with figure same miconductor wafer pcb board	USPAT			2007/05/17 18:13
114	178 (re	cuit) eference wafer alignment) near2 ark with figure same	US-PGPUB USPAT	; OR	ON	
L15	(s	emiconductor wafer)	US-PGPUI USPAT	3; OR	ON	2007/05/17 18:13
S1		eak near2 jong-sung.in.	US-PGPU USPAT	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	OFF	2006/12/14 12:52
<b>52</b>	/·	md.asn. applied.asn. kla.asn. orbotech.asn.	US-PGPU USPAT US-PGPU	,	ON	2006/12/14 12:52
53 S4	2	alignment near2 mark and S2 alignment near2 mark with	USPAT US-PGP USPAT	UB; OR	ON	2007/05/17 17:4
S5	575	(expand\$3 magnif\$7 expansion zoom\$3 focus#3 enlarg\$5) and Si 382/164,ecis.			OV	2006/12/18 12:4
S6	16	alignment near2 mark with (expand\$3 magn.f\$7 expansion zoom\$3 focus\$3 enlarg\$5) and S	US-PG USPAT	• 1	ON	2007/05/16 09:
S7	1388	alignment near? mark with (expand\$3 mac nif\$7 expansion zoom\$3 focus: 3 enlarg\$5)	US-PG USPAT		OI	2006/12/14 13:
S8	1226	alignment ne r2 mark with (expand\$3 n.agnif\$7 expansion zoom\$3 focus\$3 enlarg\$5) and (semiconductor wafer PCB circuit adj board)	US-PG USPAT		Of	N 2006/12/14 13:
S9	14	alignment near2 mark with (expands 3 magnif\$7 expansion zoom\$3 iocus\$3 enlarg\$5) same (aligment mark) with (identif\$6 recogn\$7 match\$3 verif\$8) near3 (pattern) and (semiconductor warPCB circuit adj board)			10	2006/12/14 13:

S10	24	("3821545"   "3861798"   "3928094"   "4815854"   "4838693").PN. OR ("5172190").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/12/14 13:18
S11	343	382/151.ccls. and (recogn\$6 identif\$7 match\$3) with (pattern mark align\$4)	US-PGPUB; USPAT	OR	ON	2006/12/14 16:13
S12	223	pattern near3 (recogni\$4 identif\$6 match\$3) with align\$4 near2 (mark symbol seal)	US-PGPUB; USPAT	OR	ON	2006/12/18 12:42
S13	575	382/151.ccls.	US-PGPUB; USPAT	OR	ON	2006/12/18 12:42
S14	17	S13 and S12	US-PGPUB; USPAT	OR ·	ON	2006/12/18 12:42
S15	9	("3898617"   "4115762"   "4566125"   "4688088"   "4794646"   "4794648"   "4878114"   "5543921"   "5627912").PN.	US-PGPUB; USPAT; USOCR	OR .	OFF	2006/12/18 13:09
S16	607	382/151.ccls.	US-PGPUB; USPAT	OR	ON	2007/05/16 09:49
S17	2	(expand\$3 magnif\$7 expansion zoom\$3 focus\$3 enlarg\$5) with (crop\$4 delet\$3) and S16	US-PGPUB; USPAT	OR.	ON	2007/05/16 09:58
S18	4	(expand\$3 magnif\$7 expansion zoom\$3 focus\$3 enlarg\$5) with (crop\$4 delet\$3) same align\$4 and "382".clas.	US-PGPUB; USPAT	OR	ON	2007/05/16 09:53
S19	94	(expand\$3 magnif\$7 expansion zoom\$3 focus\$3 enlarg\$5) with (crop\$4 delet\$3) same (recogni\$5 identif\$7 match\$3) and "382".clas.	US-PGPUB; USPAT	OR	ON	2007/05/16 09:54
S20	28	(expand\$3 magnif\$7 expansion zoom\$3 focus\$3 enlarg\$5) with (crop\$4 delet\$3) same (recogni\$5 identif\$7 match\$3) and "382".clas. and align\$4	US-PGPÚB; USPAT	OR	ON	2007/05/16 09:58
S21	25	(expand\$3 magnif\$7 expansion zoom\$3 fo us\$3 enlarg\$5) and (crop\$4 do let\$3) and S16	US-PGPUB; USPAT	OR	ON	2007/05/16 10:07
S22	16	(expand\$: magnif\$7 expansion zoom\$3 focus\$3 enlarg\$5) same (crop\$4 delet\$3) same (align\$4 mark match identif\$7 recogn\$5) and 382/135-154,184,190,199,287.ccls.	US-PGPUB; USPAT	OR	ON	2007/05/16 10:16
S23	30	(expand\$3 magnif\$7 expansion zoom\$3 focus\$3 enlarg\$5) same (crop\$4) and 382/135-154,184,190, 199,287.ccls.	US-PGPUB; USPAT	OR	ON	2007/05/16 16:57

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S24	23	S23 not S22	US-PGPUB; USPAT	OR	ON	2007/05/16 10:27
S25	0	search near3 area with zoom\$3 with	US-PGPUB; USPAT	OR	ON	2007/05/16 10:28
S27	0	crop\$4 wafer same alignment near2 mark	US-PGPUB; USPAT	OR	ON	2007/05/16 17:08
S28	6	with zoom\$3 alignment near2 mark with zoom\$3	US-PGPUB; USPAT	OR	ON	2007/05/16 17:22
S29	386	alignment near2 mark with enlarg\$6	1	OR	ON	2007/05/16 17:10
S30	168	alignment near2 mark with enlarg\$6 same (wafer semiconductor circuit		OR	ON	2007/05/16 17:14
S31	46	pcb) alignment near2 mark with enlarg\$6 same (wafer semiconductor circuit pcb) and alignment near2 mark with (identif\$7 recogni\$6 distinguish\$3 match\$3)	1 USPAT		ON	2007/05/16 17:14
<b>S32</b>	19	("20010046315"   "20020175300"   "20020176096"   "20040114143"   "20040129900"   "4669883"   "4777641"   "4883359"   "4971444"   "5133603"   "5659384"   "5680200"   "5760411"   "6151120"   "6285033"   "6493065"   "6529625"   "6563573"   "6844918").PN.	USOCR	; OR	OFF	
533	1	2 (enlarg\$4 magnif\$5) and S32	US-PGPUE USPAT; USOCR	3;   OR	OFF	2007/05/16 17:20
S34	1	2 (enlarg\$4 magnif\$5 zoom\$3) and S32	US-PGPU USPAT; USOCR	B; OR	OFF	2007/05/16 17:20
S35	11	reference near2 mark with (zooms enlarg\$3 magnif\$7) same align\$4	US-PGPU USPAT	B; OR	ON	2007/05/16 17:28
S36	,	reference near2 mark with (zooms enlarg\$3 magnif\$7) and mark with (identif\$7 recogni\$6 distinguish\$3 match\$3) and (wafer semiconduction circuit pcb) with align\$4	us-pgpu uspat	B; OR	ON	2007/05/16 17:29
S37	7	reference near2 mark with (zoom enlarg\$3 magnif\$7) and mark wit (identif\$7 recogni\$6 distinguish\$3 match\$3) and (wafer semiconductircuit pcb) with align\$4 not S31	tor		ON	2007/05/17 10:50
S38	3	(reference wafer alignment) near mark near7 (zoom\$3)	2 US-PGPU USPAT	JB; OR	ON	2007/05/17 17:46

S39	1	"20040037458"	US-PGPUB; USPAT	OR	ON	2007/05/17 17:35
S40	6	(reference wafer alignment) near2 mark near7 (window adj frame)	US-PGPUB; USPAT	OR	ON	2007/05/17 10:52
S41	1089	coarse same fine same (alignment reference wafer key) near2 mark	US-PGPUB; USPAT	OR .	ON	2007/05/17 11:06
S42	1	coarse near3 search same fine same (alignment reference wafer key) near2 mark	US-PGPUB; USPAT	OR	ON	2007/05/17 11:05
S43	0	("2005/0156334").URPN.	USPAT	OR	OFF	2007/05/17 11:06
S45	. 6	coarse same fine same (alignment reference wafer key) near2 mark and 382/145-151.ccls.	US-PGPUB; USPAT	OR	ON	2007/05/17 11:10
S46	. 0	bishop.in. and 382/151.ccls.	US-PGPUB; USPAT	OR	ON	2007/05/17 11:07
S47	4	bishop.in. and 382/145-152.ccls.	US-PGPUB; USPAT	OR	ON	2007/05/17 11:07
S48	1069	coarse same fine same (alignment reference wafer key) near2 mark and (wafer semiconductor circuit pcb) and align\$4	US-PGPUB; USPAT	OR	ON	2007/05/17 11:11
S49	24	coarse same fine same (alignment reference wafer key) near2 mark same (zoom\$3 enlarg\$4 magnif\$7) and (wafer semiconductor circuit pcb) and align\$4	US-PGPUB; USPAT	OR	ON	2007/05/17 11:16
S50	103	382/151.ccls. and alignment near2 mark	US-PGPUB; USPAT	OR	ON	2007/05/17 11:17
S51	0	382/151.ccls. and alignment near2 mark with "fig."	US-PGPUB; USPAT	OR	ON	2007/05/17 11:16
S52	0	382/151.ccls. and alignment near2 mark with fig	US-PGPUB; USPAT	OR	ON	2007/05/17 11:17